

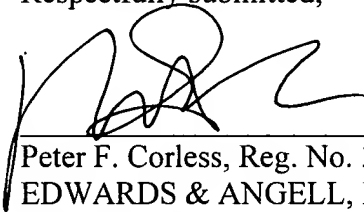
26. The circuit arrangement of claim 22 wherein in a manner dependent on the ratio of a reading speed of a read pointer to a writing speed of a write pointer and a relative position of the write pointer in a writing area holding the currently written insertion picture, the decision device decides whether the currently written insertion picture (Kj) or the immediately preceding insertion picture (Kj-1) is read out.

✓  
REMARKS

Claims 1-13 have been cancelled without prejudice, and claims 14-26 have been added.  
No new matter has been added.

Early consideration and allowance of the application are earnestly solicited.

Respectfully submitted,

  
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